INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known	
Application Number	09/487,483
Filing Date	January 19, 2000
First Named Inventor	Masue SHIBA
Art Unit	2134
Examiner Name	M. Simitoski
Attorney Docket Number	04329.2217

Sheet	1	of	1
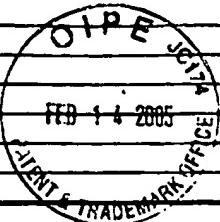
U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS

Examiner Initials	Cite No.	Document Number — Number-Kind Code[2] *(if known)*	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

Note: Copies of the U.S. Patent Documents are not Required in IDS filed after October 21, 2004

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.[1]	Foreign Patent Document — Country Code[3] Number[4] Kind Code[5] *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation[6]
MS		DE-196 44 688 A1	04-30-1998			

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation[6]
MS		W. Drescher et al., VLSI Architectures for Multiplication in GF(2^m) for Application Tailored Digital Signal Processors, VLSI Signal Processing, IX, 1996., WORKSHOP ON SAN FRANCISCO, CA, USA 10/30 – 11/1/1996. ISBN: 0-7803-3134-6.	
MS		Search Report issued by French Patent Office on 12-16-04 in French Application No. 0000680	

Examiner Signature	*[signature]*	Date Considered	7/12/05

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